



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Takahiro Yamaguchi, et al.
Serial No.: 09/811,153
Filing Date: March 16, 2001
Title: Apparatus for and Method of
Measuring Jitter
Conf. No. 5797

Examiner: Dac V. Ha

Art Unit: 2634

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SUBMISSION OF FORMAL REPLACEMENT DRAWINGS

Sir:

Applicants submit herewith five sheets of formal replacement drawings for the U.S. patent application captioned above.

These drawings incorporate the legend "Prior Art" on Figures 1-4, 5A, 5B, 6 and 10A as requested by the Examiner in the Office Action mailed June 21, 2004.

Respectfully submitted,


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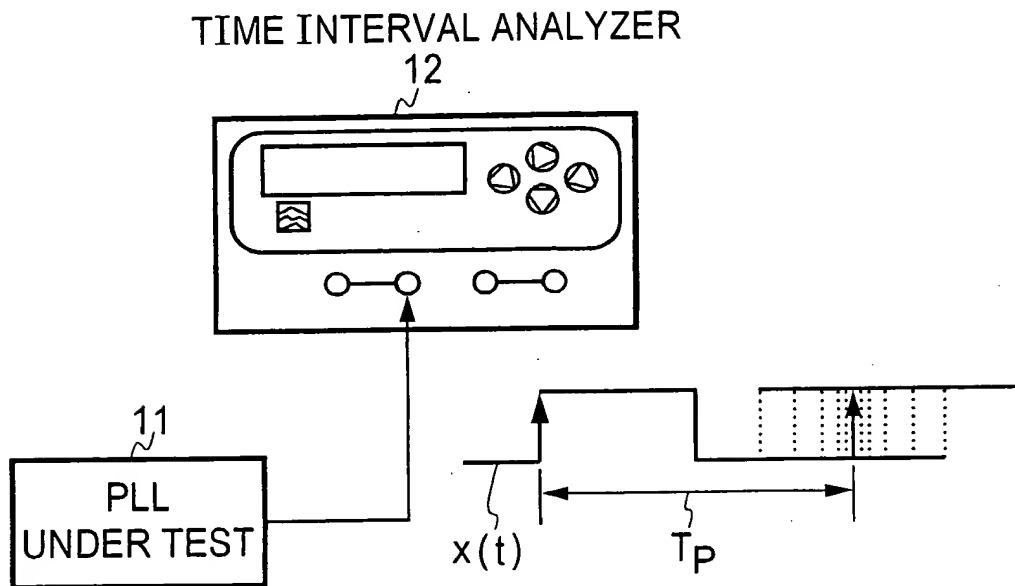
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Enc. Replacement drawings (5)
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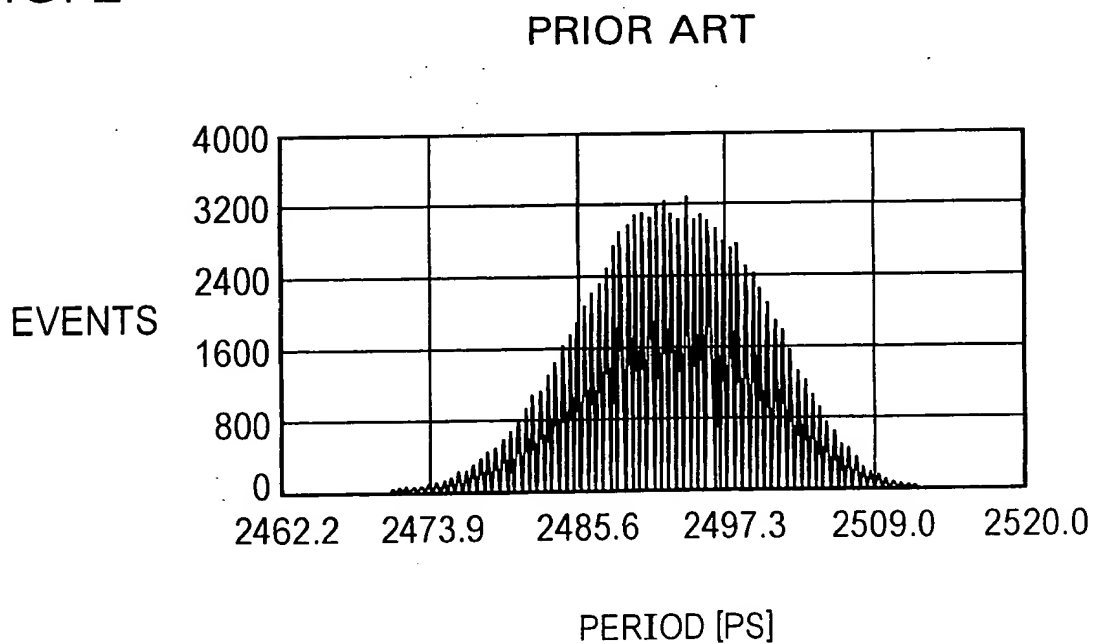


FIG. 1



PRIOR ART

FIG. 2



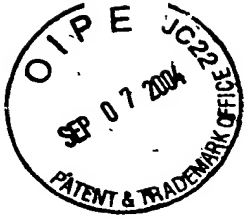


FIG. 3

PRIOR ART

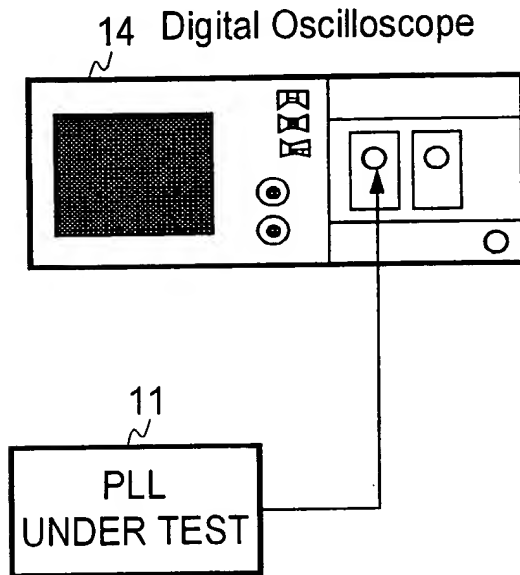


FIG. 4

PRIOR ART

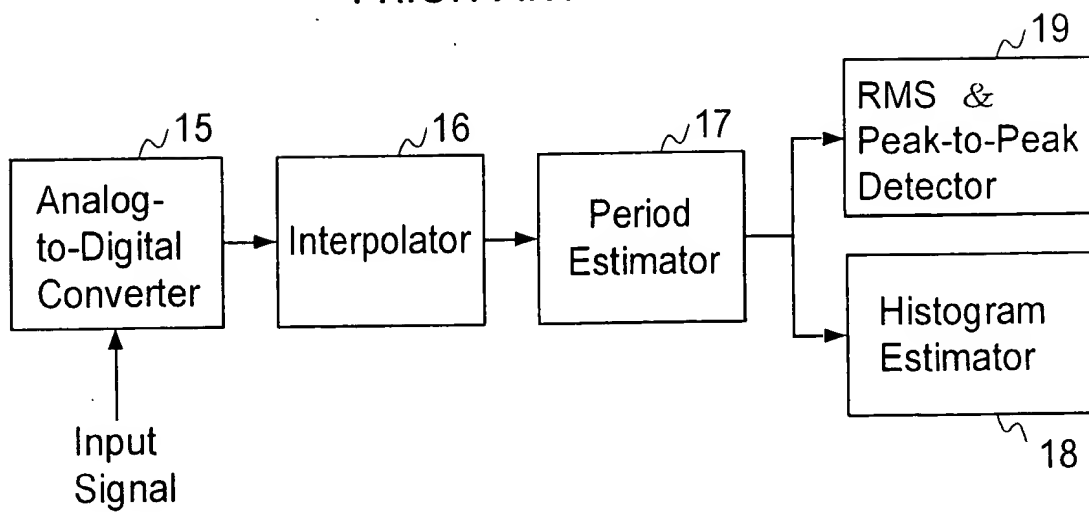




FIG. 5A

PRIOR ART

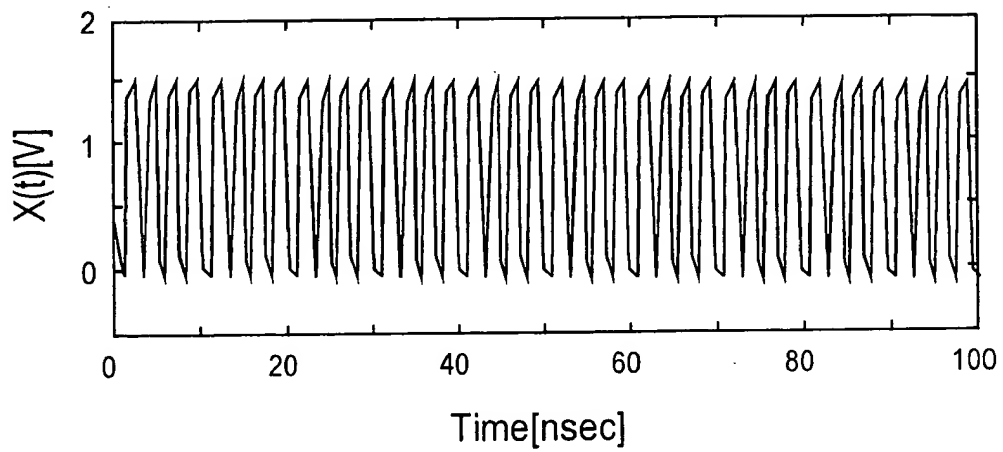
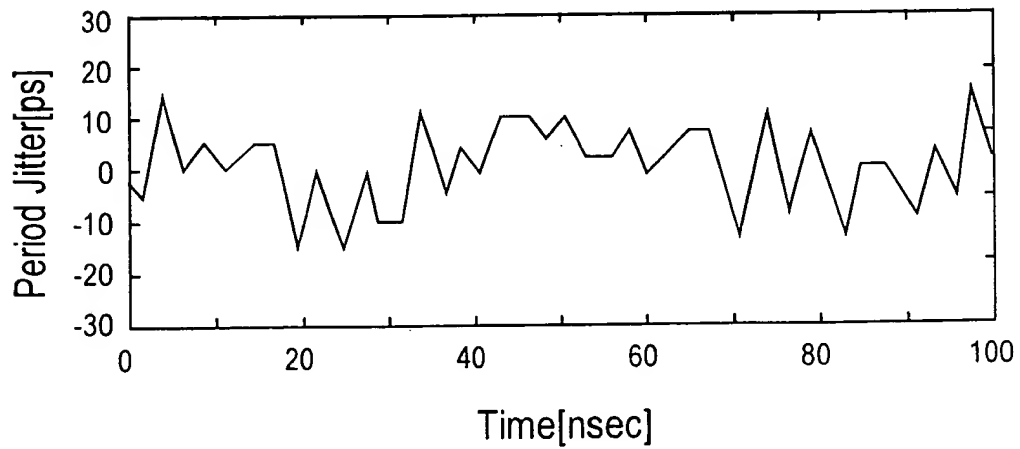


FIG. 5B

PRIOR ART



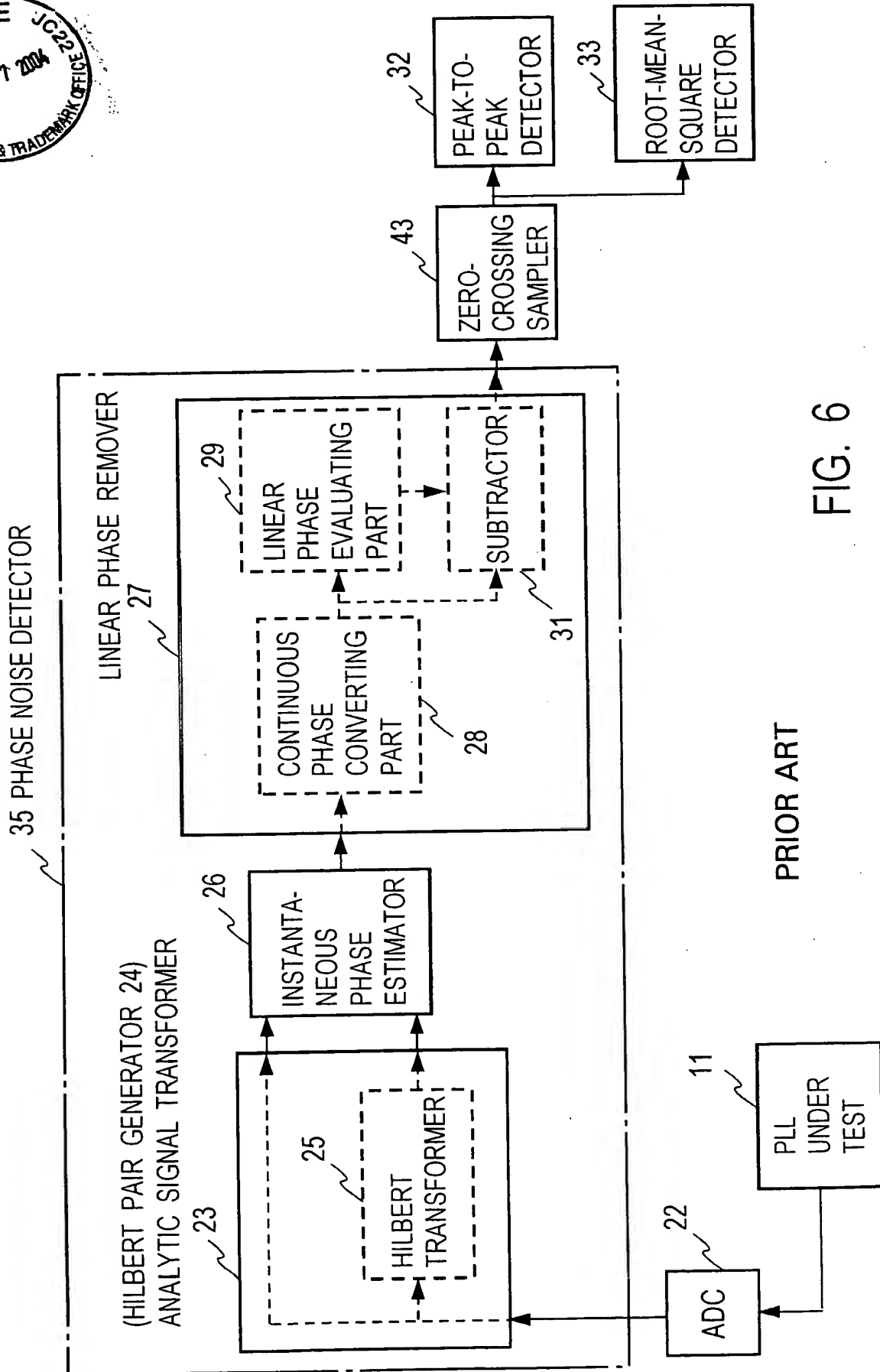
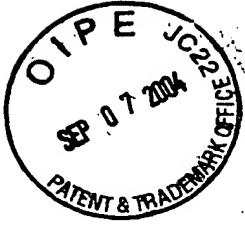




FIG. 10A

PRIOR ART

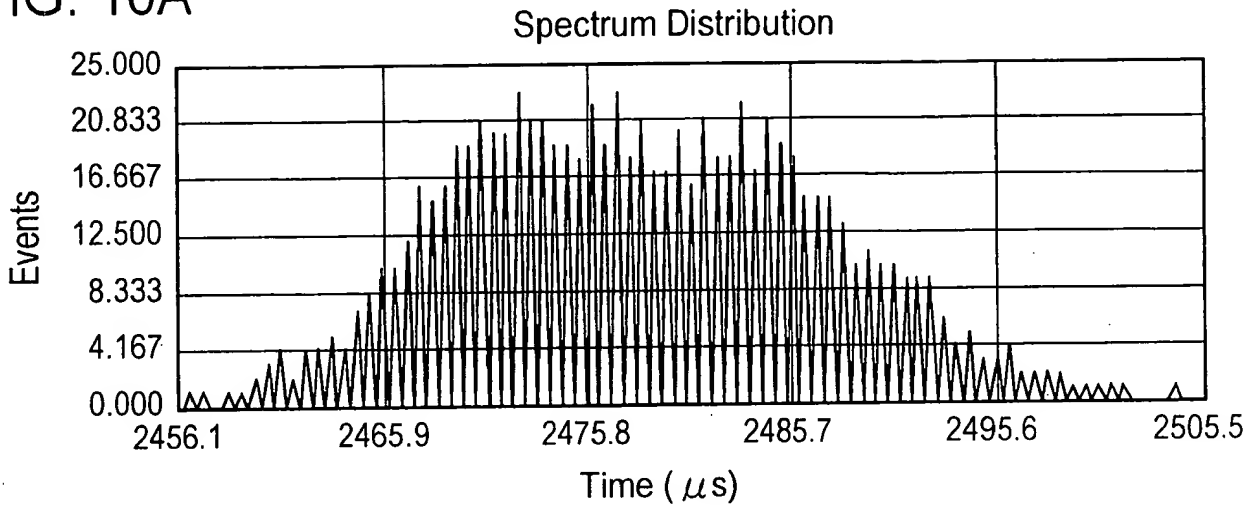


FIG. 10B

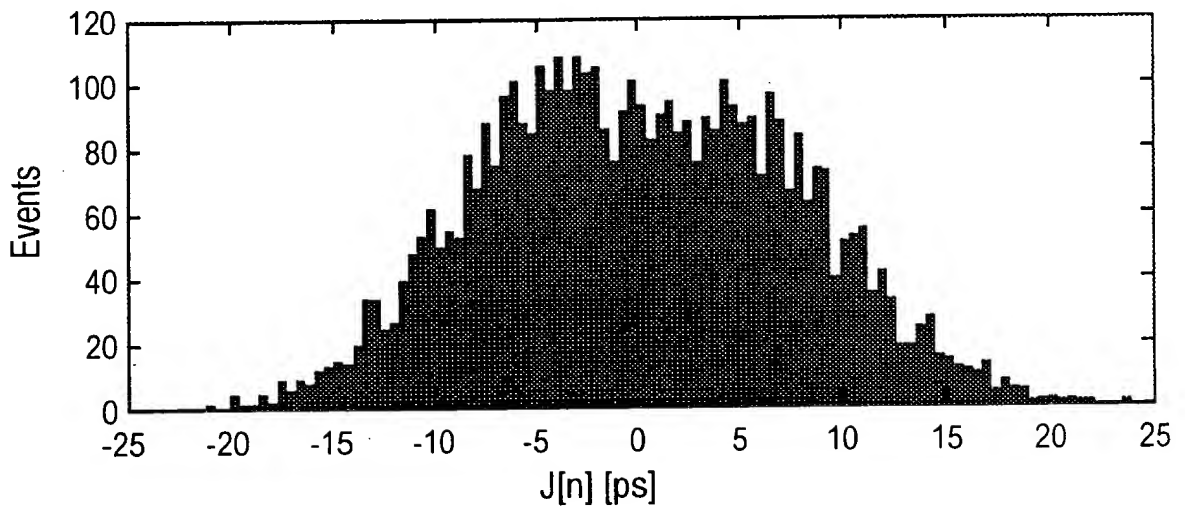


FIG. 11

Method	No. of Events	J_{RMS}	J_{PP}
Time Interval Analyzer	10,000	7.72 ps	48.2 ps
Corrected $\Delta\phi$ Method	4,696	7.48 ps	45.2 ps
Difference	-53%	-3.1%	+0.4%